

Practitioner's Docket No.: TSL-125

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant: Yakov Roizin  
Assignee: Tower Semiconductor Ltd.  
Serial No.: 10/659,031  
Filed: 9/9/2003  
For: "Protection Against In-Process Charging In Silicon-Oxide-Nitride-Oxide-Silicon (SONOS) Memories"

Art Unit.: 2818  
Examiner: Andy Huynh

November 23, 2004

VIA FACSIMILE - (703) 872-9306

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the outstanding Office Action dated October 28, 2004, in which the Examiner imposed a restriction requirement to election of invention for the above-referenced application, Applicant elects to prosecute the invention as in Species I, Claims 1-13 and 22-29 drawn to a pre-metal dielectric structure for a silicon-nitride-oxide-silicon (SONOS) memory transistor and manufacturing method, without traverse.

Applicant reserves the right to file divisional applications on the non-elected claims.

Respectfully submitted,



E. Eric Hoffman,  
Reg. No. 38,186  
Attorney for Applicant(s)

Customer No.: 022888  
Tel. No.: (925) 895-3545  
Fax No.: (408) 451-5908

CERTIFICATE OF TRANSMISSION (37 C.F.R. 1.8(a))

I hereby certify that, on the date shown below, this correspondence is being transmitted by facsimile to the Patent and Trademark Office.

Date: 11/23/04 Signature: Carrie Reddick

Resent 11/26/04 Carrie Reddick